Serial Number: 10/643,585 Filing Date: August 18, 2003

Title: LATENCY TOLERANT DISTRIBUTED SHARED MEMORY MULTIPROCESSOR COMPUTER

## IN THE DRAWINGS

Fig. 9 has been amended to add item 940 (translation look-aside buffer (TLB)). New Figs. 11 and 12 are added. A complete set of replacement drawings (Figs. 1-12) is filed herewith.